

501.20699VC3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): R. HORI, et al.  
Serial No.: 08/371,973  
Filed: January 12, 1995  
For: SEMICONDUCTOR INTEGRATED CIRCUIT WITH  
VOLTAGE LIMITER HAVING DIFFERENT OUTPUT  
RANGES FORM NORMAL OPERATION AND PERFORMING  
OF AGING TEST  
Group: 2306  
Examiner: S. Baker

55 JUN 28 1996  
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TOLSON

LETTER SUBMITTING FORMAL DRAWINGS

Honorable Commissioner of  
Patents and Trademarks  
Washington, D.C. 20231


June 28, 1996

Sir:

In response to the attachment to the Notice of Allowance, submitted herewith are Fourteen (14) sheets of formal drawings illustrating Figs. 1-12, 13A-13C, 14A-14C, 15-28, 29A-29B and 30-36 in connection with the above-identified application.

Respectfully submitted,

CIB/hpg  
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